

**Notice of Allowability**

Application No.

10/748,242

Examiner

Mujtaba K. Chaudry

Applicant(s)

YAMANAKA ET AL.

Art Unit

2133

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 7/24/2006.
2. ☒ The allowed claim(s) is/are 10-23.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some\* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☒ Certified copies of the priority documents have been received in Application No. 09147663.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.


Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 7/24/2006
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

  
ALBERT DECADY  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2100

## **DETAILED ACTION**

### ***Information Disclosure Statement***

The references listed in the information disclosure statement (IDS) submitted on July 24, 2006, 2006 have been considered. The submission is in compliance with the provisions of 37 CFR 1.97. Although certain reference were not considered previously, Examiner has now considered them in light of them being previously submitted in previous related applications.

### ***Double Patenting***

The terminal disclaimer filed is accepted and therefore the double-patenting rejection with respect to USPN 6735714 has been withdrawn.

## **REASONS FOR ALLOWANCE**

Claims 10-23 are allowed. The following is an Examiner's statement of reasons for allowance:

Independent claim 10 of the present application teaches a digital signal processor, comprising: an arithmetic logic unit configured to perform register-register arithmetic logic operations; a plurality of registers configured to store data, wherein the arithmetic logic unit is used to compare a first data with a second data and to output one of the first data and the second data based on a result of the comparison of the first data and the second data, in parallel with a comparison of a third data with a fourth data and an output of one of the third data and the fourth data based on a result of the comparison of the third data and the fourth data, wherein the output one of the first data and the second data is provided as first part of processing data and the output

Art Unit: 2133

one of the third and the fourth data is provided as a second part of processing data lower than the first part, and wherein at least one the plurality of registers stores the result of the comparison of the first data and the second data and the result of the comparison of the third data and the fourth data. The foregoing limitations are not found in the prior arts of record. Particularly, none of the prior arts of record teach nor fairly suggest, "...to compare a first data with a second data and to output one of the first data and the second data based on a result of the comparison of the first data and the second data, in parallel with a comparison of a third data with a fourth data and an output of one of the third data and the fourth data based on a result of the comparison of the third data and the fourth data, wherein the output one of the first data and the second data is provided as first part of processing data and the output one of the third and the fourth data is provided as a second part of processing data lower than the first part, and wherein at least one the plurality of registers stores the result of the comparison of the first data and the second data and the result of the comparison of the third data and the fourth data".


Independent claim 17 includes similar limitations of independent claim 10 and therefore is allowed for similar reasons.


Dependent claims 11-16 and 18-23 depend from allowable independent claims and inherently include limitations therein and therefore are allowed as well.

Art Unit: 2133

Any inquiries concerning this communication should be directed to the examiner, Mujtaba Chaudry who may be reached at 571-272-3817. The examiner may normally be reached Mon – Thur 6:30 am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, please contact the examiner's supervisor, Albert DeCady at 571-272-3819.

  
Mujtaba Chaudry  
Art Unit 2133  
July 31, 2006

  
ALBERT DECADY  
SUPERVISORY PATENT EXAMINER  
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